

ABSTRACT OF THE DISCLOSURE

To provide a large-scale integrated-circuit apparatus having a CPU and a plurality of circuit blocks to be operated in accordance with clock signals, which includes the circuit blocks for receiving reset signals to perform initialization and outputting reset completion signals and a CPU for logic-operating the initialization completion signals output from the circuit blocks and outputting a signal for canceling resetting to the circuit blocks in accordance with the logic-operation result.

09915902 080204  
FOUO "20657660"